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U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

APPL NUM 10087063	FILING DATE 03/01/2002	CLASS 438	SUBCLASS 438	GAU 2842	EXAMINER H Lee
<b>**APPLICANTS:</b> Nam Dong-Seok; Kim Ji-Soo; Chae Yun-Sook;					
<b>BEST AVAILABLE COPY</b>					
<b>**CONTINUING DATA VERIFIED:</b> <i>Lee</i> <i>NONE</i>					
<b>** FOREIGN APPLICATIONS VERIFIED:</b> <i>Lee</i> REPUBLIC OF KOREA 2001-48740 08/13/2001 <i>Yes</i>					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO			
35 USC 119 conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		8021-89 (SS-16177-US)			
Verified and Acknowledged Examiners's initials <i>Lee</i>					
TITLE : Semiconductor device having self-aligned contact pads and method for manufacturing the same					
<small>U.S. DEPT. OF COMM./PAT. &amp; TM-PTO-436L (Rev. 12-04)</small>					

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>Assistant Examiner</b>	<b>CLAIMS ALLOWED</b>	
			Total Claims	Print Claim for O.G.
<b>ISSUE FEE</b>		<b>Primary Examiner</b>	<b>DRAWING</b>	
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>PREPARED FOR ISSUE</b>	<b>Application Examiner</b>	
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